

Notice of References CitedApplication/Control No.
09/839,624Applicant(s)/Patent Under
Reexamination
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,193,175	03-1993	Cutts et al.	714/11
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
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FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tullsen, D.M. Eggers, S.J. Levy, H.M.; Simultaneous Multithreading: Maximizing on-chip Parallelism; June 22-24, 1995; Computer Architecture 1995; Pages 392-403
	V	The Authoritative Dictionary of IEEE Standard Terms; IEEE; 7th Edition; 2000; Page 519
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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